



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 2358

Bib Data Sheet

SERIAL NUMBER 10/756,366	FILING DATE 01/14/2004 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. TAI 146
-----------------------------	---------------------------------------	--------------	------------------------	-----------------------------------

APPLICANTS

Hiroaki Nakano, Tokyo, JAPAN;

** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2003-111904 04/16/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/15/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after <i>James M. Jee</i> <i>MM</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
--	--	------------------------------	------------------------	-----------------------	----------------------------

ADDRESS

23995
 RABIN & Berdo, PC
 1101 14TH STREET, NW
 SUITE 500
 WASHINGTON , DC
 20005

TITLE

Circuit board for mounting a semiconductor chip and manufacturing method thereof

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	--